

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of

HAYASHI et al.

Application Number: 09/976,052

Filed: October 15, 2001

For: LEVEL SHIFT CIRCUIT AND
SEMICONDUCTOR INTEGRATED CIRCUIT

Attorney Docket No. HITA.0110



Art Unit 2816

Examiner
Nguyen, Minh T.

RECEIVED

MAR 09 2004

Honorable Assistant Commissioner
for Patents

Washington, D.C. 20231

E/UTK
PLEASE
3/18/04

RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

This is in response to the office action dated October 28, 2003, in the above identified application, to which the period for response will expire on February 28, 2004, with a one-month extension fee filed herewith. Please amend the above identified application as follows: